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October 3, 2007

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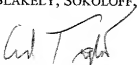
Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 7,225,349
Issued: May 29, 2007
For: **POWER SUPPLY VOLTAGE DROOP
COMPENSATED CLOCK MODULATION FOR
MICROPROCESSORS**
Inventor: Tam, et al.
Our File No. 42390.P15905

Dear Sir:

Enclosed is the Certificate of Correction for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



Edwin H. Taylor
Reg. No. 25,129

EHT/sn
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,225,349
DATED : May 29, 2007
INVENTOR(S) : Tam et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 1, at line 41, delete "yea" and insert --Vcc--.

In column 5, at line 49, delete "ore" and insert --are--.

In column 5, at line 58, delete "fail" and insert --fall--.

In column 6, at line 47, delete "Further" and insert --further--.

In column 8, at line 9, delete "o" and insert --to--.

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PATENT NO. 7,225,349

Certificate of Correction (PTO Form 1050)-Amended